**Course Specification**

(IS 435 Information Centers Management)

|  |  |
| --- | --- |
| **University:** | Helwan University |
| **Faculty:** | Faculty of Co؛mputers & Information |
| **Department:** | Information systems |

**1. Course Data**

|  |  |
| --- | --- |
| **Code:** | **IS 435** |
| **Course title:** | Information Centers Management |
| **Level:** | 4 |
| **Specialization:** | Information systems |
| **Credit hours:** |  |
| **Number of learning units (hours):** | (3) theoretical (2) tutorial |

**2. Course Objective**

Operations in information centers, organizational structure and management functions. Personnel recruitment, advancement and appraisal. Budgeting, charges and financial analysis. Site selection and preparation, Hardware and Software acquisition. Information centre standards, procedures and workflow. Job scheduling, resource allocation, users’ needs, data communication and performance evaluation. IS project management techniques, project appraisal and selection.

**3. Intended Learning Outcomes:**

1. **Knowledge and Understanding**

A21. Describe Management process for Software Projects.

1. **Intellectual Skills**

B14. Develop the act of getting people together to accomplish desired goals and objectives (Management skills).

B17. Examine problems carefully and effectively.

1. **Professional and Practical Skills**

C16. Plan different management techniques.

1. **General and Transferable Skills**

D3. Practice Leadership and managing.

D4. Specify and arrange Report writing steps.

**4. Course contents**

|  |  |  |  |
| --- | --- | --- | --- |
| **Topic** | **No. of hours** | **Lecture** | **Tutorial/ Practical** |
| Introduction to digital computers with a brief history | 3 | 1 | 1 |
| Performance Metrics | 3 | 1 | 1 |
| Assembly Language Interface and Syntax | 3 | 1 | 1 |
| Digital Logic Design | 6 | 2 | 2 |
| CPU Design | 6 | 2 | 2 |
| Memory Design | 6 | 2 | 2 |
| I/O interfacing | 6 | 2 | 2 |
| Parallel processing | 6 | 2 | 2 |

**Mapping contents to ILOs**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Topic | Intended Learning Outcomes (ILOs) | | | |
| Knowledge and understanding | Intellectual Skills | Professional and practical skills | General and Transferable skills |
| Introduction to digital computers with a brief history |  | B14 |  |  |
| Performance Metrics | A21 |  |  |  |
| Assembly Language Interface and Syntax | A21 | B14,B17 | C16 | D4 |
| Digital Logic Design | A21 | B14 |  |  |
| CPU Design | A21 |  | C16 | D4 |
| Memory Design | A21 |  | C16 | D3 |
| I/O interfacing | A21 |  |  |  |
| Parallel processing |  | B17 |  | D4 |

**5. Teaching and Learning Methods**

Class Lectures

Discussion

Readings

**6. Teaching and Learning Methods for students with limited capability**

Using data show

e-learning management tools

**7. Students Evaluation**

**a) Used Methods**

Case studies

**b) Time**

Assessment 1: Test 1 Week 4

Assessment 2: Test 2 Week 7

Assessment 3: Midterm Exam Week 10

Assessment 4: final written exam Week 14

**c) Grades Distribution**

Mid-Term Examination 20%

Final-term Examination 60%

Other types of assessment 20 %

Total 100%

Any formative only assessments

**List of Books and References**

**a) Notes**

Course Notes

Handouts

**b) Mandatory Books**

- Barry Wilkinson and Michael Allen. Parallel Programming: Techniques and Applications

Using Networked Workstations and Parallel Computers. Prentice Hall, 1999.

**c) Suggested Books**

- Selim G. Akl. The Design and Analysis of Parallel Algorithms. Prentice Hall, 1989.

- Kenneth A. Berman and Jerome L. Paul. Fundamentals of Sequential and Parallel Algorithms. PWS, 1997.

**d) Other publications**

**Course Coordinator:**  Prof. Dr. Yehia Helmy

**Chairman of the Department:** Prof. Dr. Yehia Helmy